

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Chin-Chi TENG et al

Application No: 10/043,458

Filed: January 9, 2002

For: CLOCK TREE SYNTHESIS FOR A  
HIERARCHICALLY PARTITIONED IC LAYOUT

Art Unit: 2812

Examiner:  
Stacy Whitmore

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REPLY TO THE OFFICE ACTION MAILED 10/09/2003

COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

Further examination and consideration of this application are  
requested in view of the following Amendments and Remarks.